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PTO/SB/21 (08-00)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/715,267
	Filing Date	November 17, 2003
	First Named Inventor	Nathan R. Brown
	Group Art Unit	1763
	Examiner Name	S. MacArthur
Attorney Docket Number		2269-4375.1US (99-1029.01US)

ENCLOSURES (check all that apply)		
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Nathan R. Brown

Serial No.: 10/715,267

Filed: November 17, 2003

For: METHODS FOR POLISHING
SEMICONDUCTOR DEVICE
STRUCTURES BY DIFFERENTIALLY
APPLYING PRESSURE TO SUBSTRATES
THAT CARRY THE SEMICONDUCTOR
DEVICE STRUCTURES

Confirmation No.: 4590

Examiner: S. MacArthur

Group Art Unit: 1763

Attorney Docket No.: 2269-4375.1US
(99-1029.01/US)

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July 7, 2005
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RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is being filed in response to the Office Action mailed on April 7, 2005, the three-month shortened statutory period for response to which expires on July 7, 2005.